



Used in Lieu of PTO/SB/08A/B
(Based on PTO 01-08 version)

Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Complete if Known	
				Application Number	10/579,766-Conf. #3580
				Filing Date	May 18, 2006
				First Named Inventor	Takamitsu Yamada
				Art Unit	2827
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	R2184.0495/P495

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶

Examiner Signature		Date Considered	
--------------------	--	-----------------	--

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	CA	TASIRAN et al.; "Linking Simulation with Formal Verification at a Higher Level"; Synergies for Design Verification; IEEE Design & Test of Computers; Vol. 21, No. 6; Jun 2004; pp. 472-482.	
	CB	CHEN et al.; "Logic of Constraints: A Quantitative Performance and Functional Constraint Formalism"; IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems; Vol. 23, No. 8; August 2004; pp. 1243-1255.	
	CC	DATTA et al.; "Assertion Based Verification Using HDVL"; Proceedings of the 17th International Conference on VLSI Design; Jan. 2004; pp. 319-325.	
	CD	PIXLEY et al.; "Functional Verification 2003: Technology, Tools and Methodology"; Proceedings 5th International Conference, IEEE; Vol. 1; Oct. 2003; pp. 1-5.	

Examiner Signature	/Vuthe Siek/	Date Considered	06/24/2008
--------------------	--------------	-----------------	------------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.